

TP501UB Repair Guide

Dec/16/ 2015

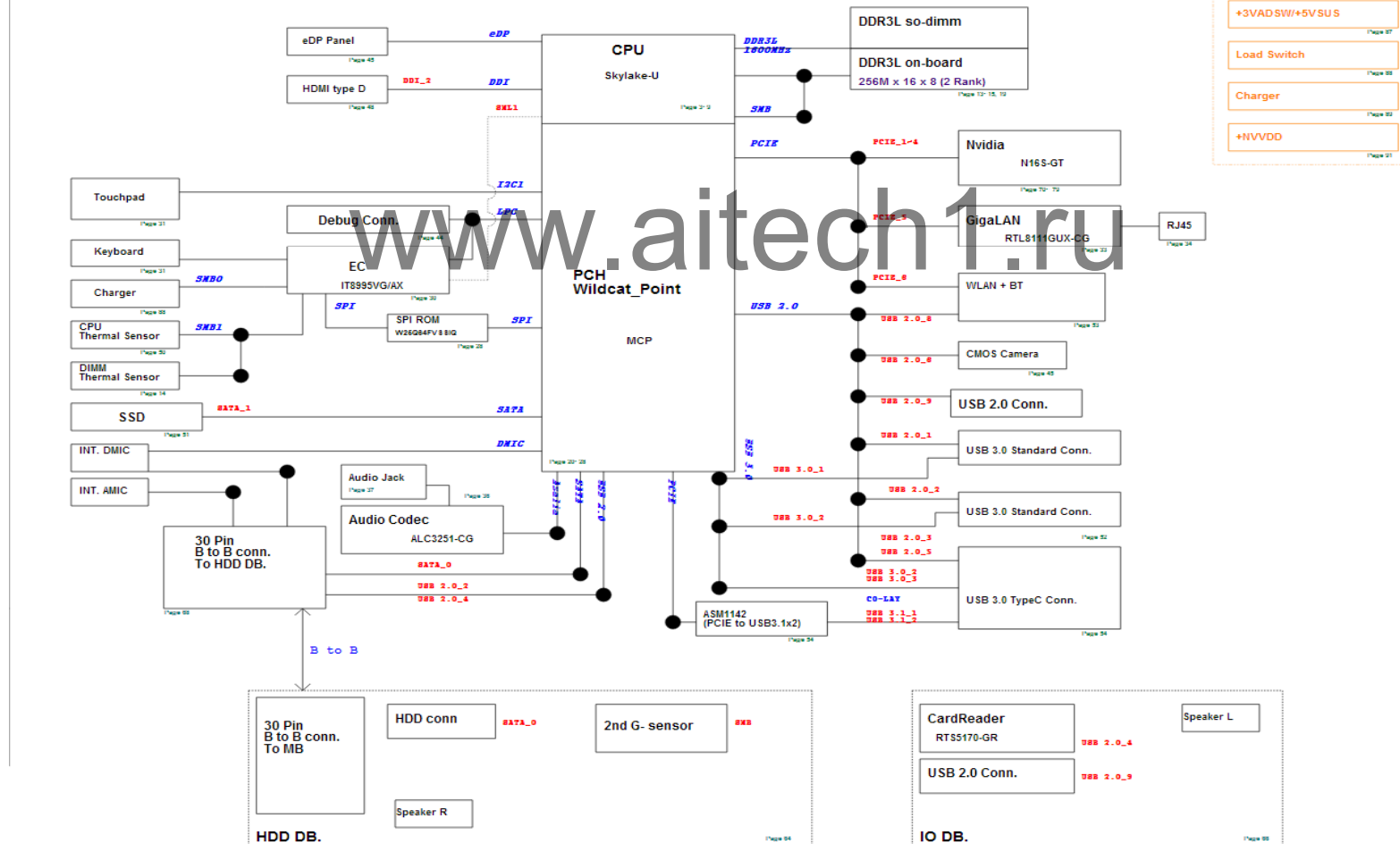
John_jin
GTSD

BLOCK DIAGRAM

TP501 SCHEMATIC Revision 0.1

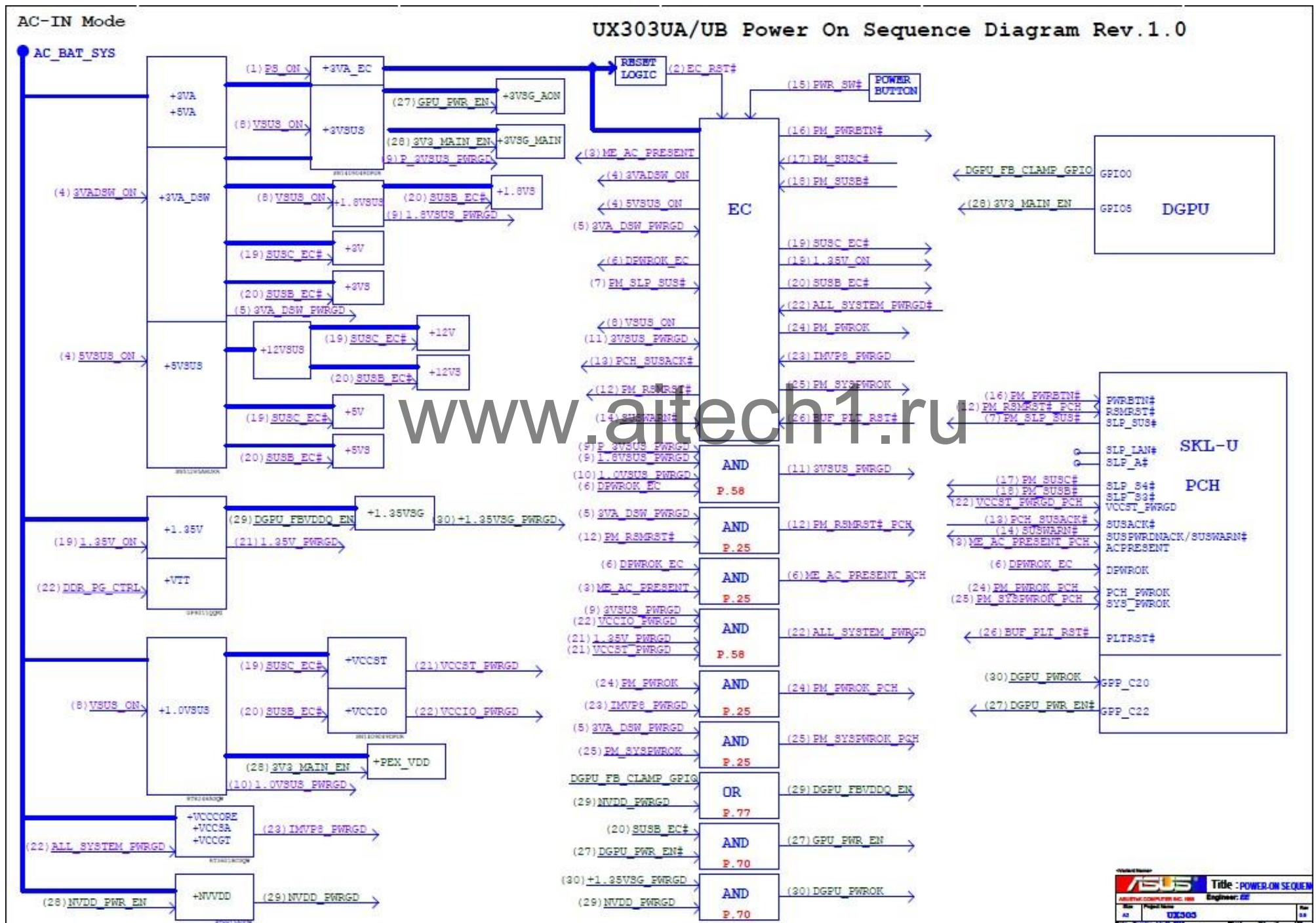
(UA: UMA)
(UB: DGPU = Nvidia N16S-GT, 940)

Non Connected Standby



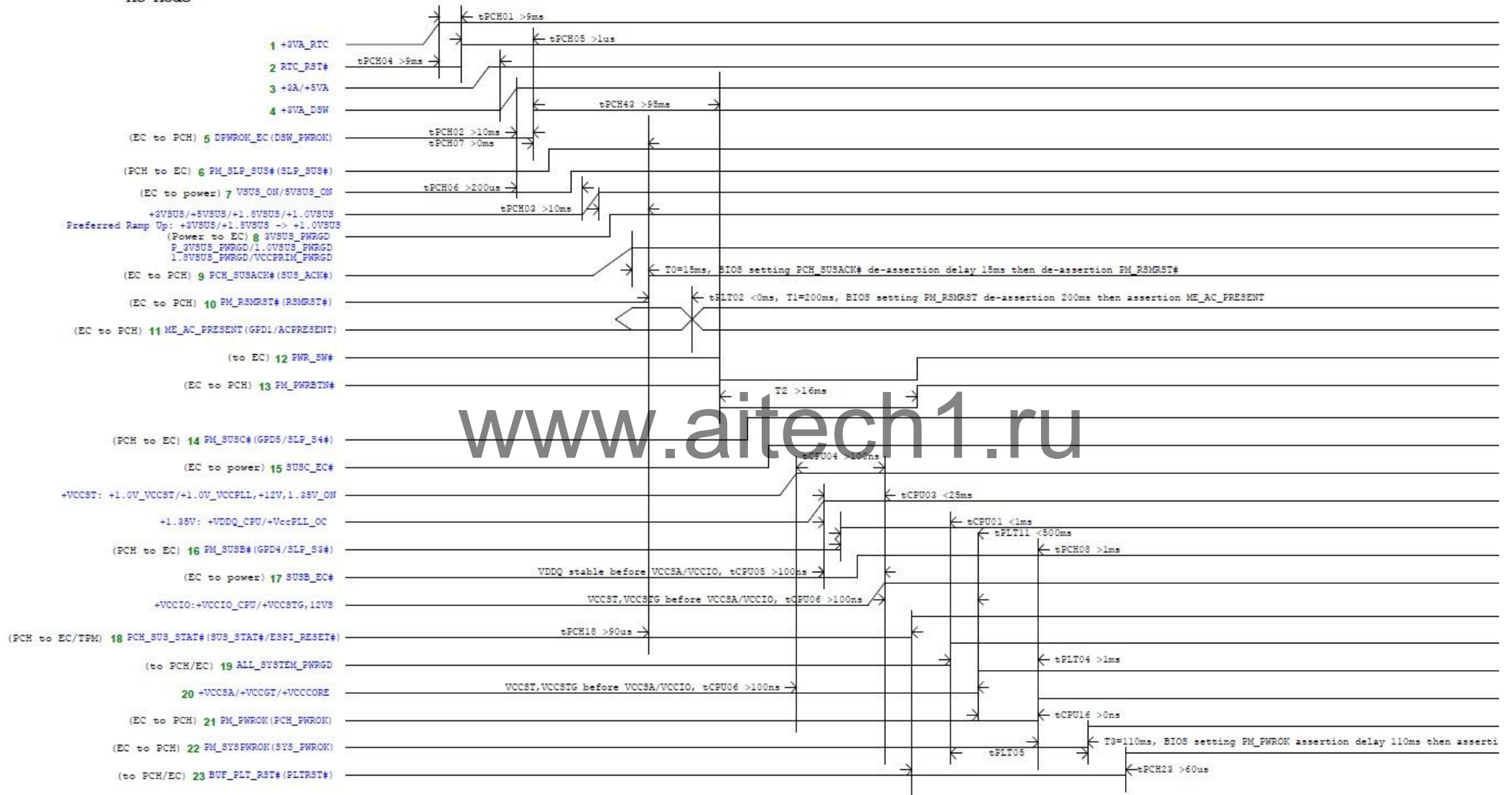
[illegible]

POWER ON SEQUENCE



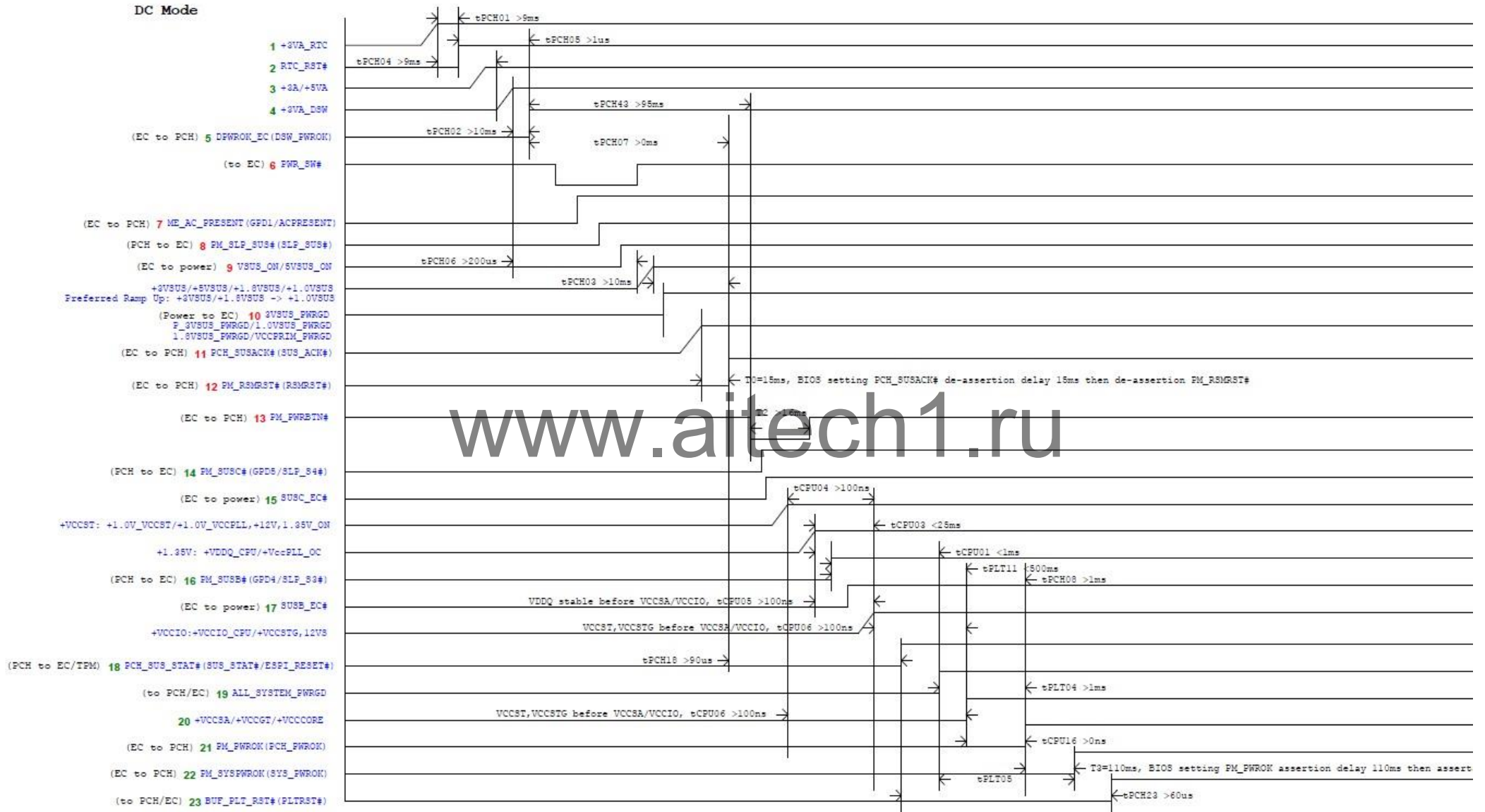
AC POWER ON SEQUENCE

AC Mode

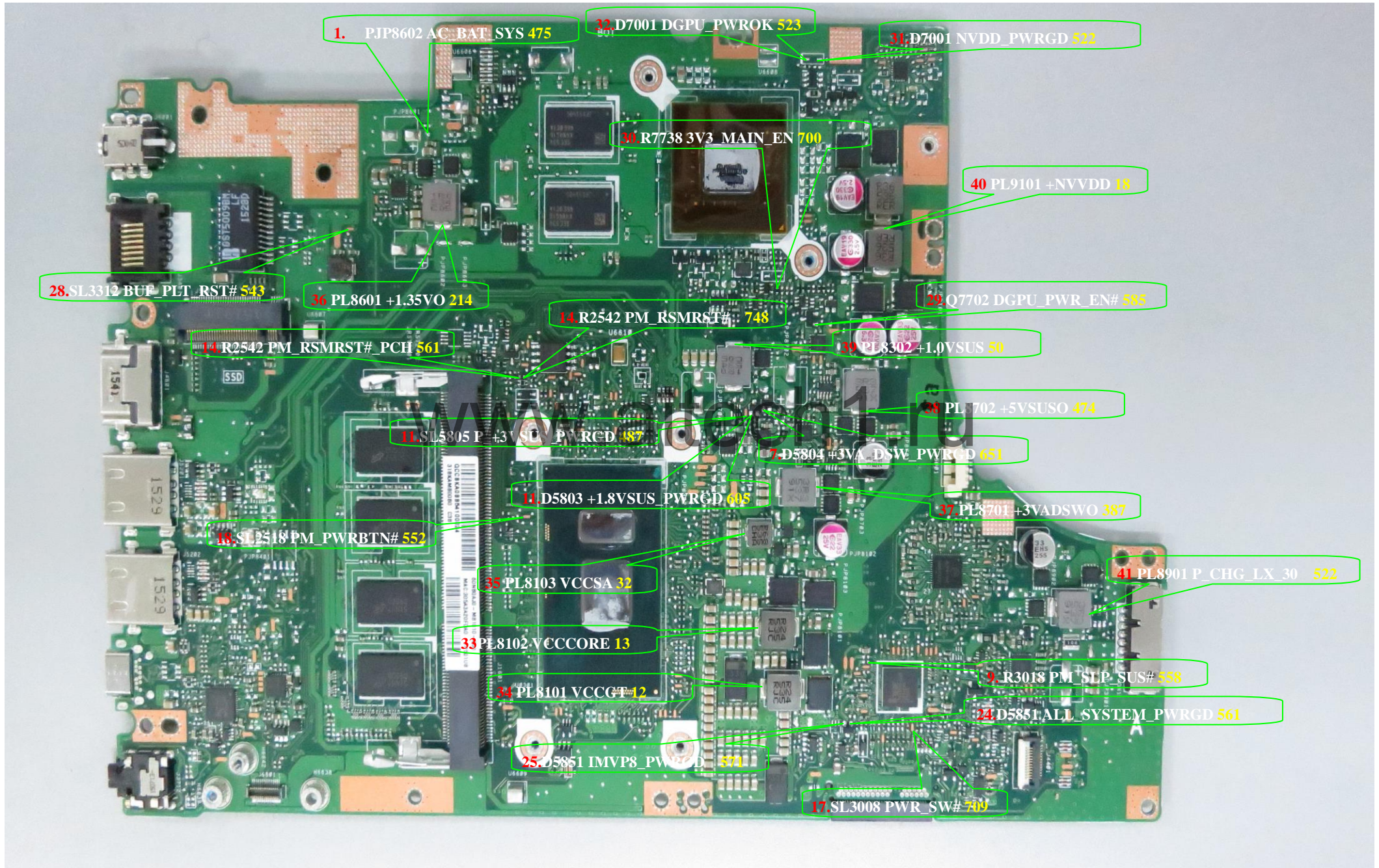


DC POWER ON SEQUENCE

DC Mode



Signal Measure Point-Bottom



Signal Measure Point-Top

